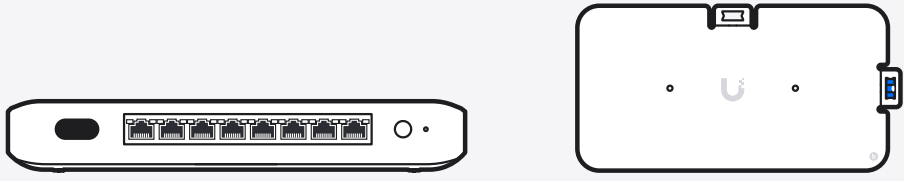




Device Bridge Switch



1x

a

1x

b



1x

c



2x

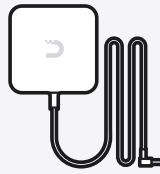
d

M3



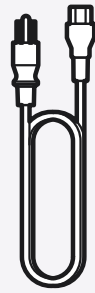
2x

e



1x

f



1x

g



